

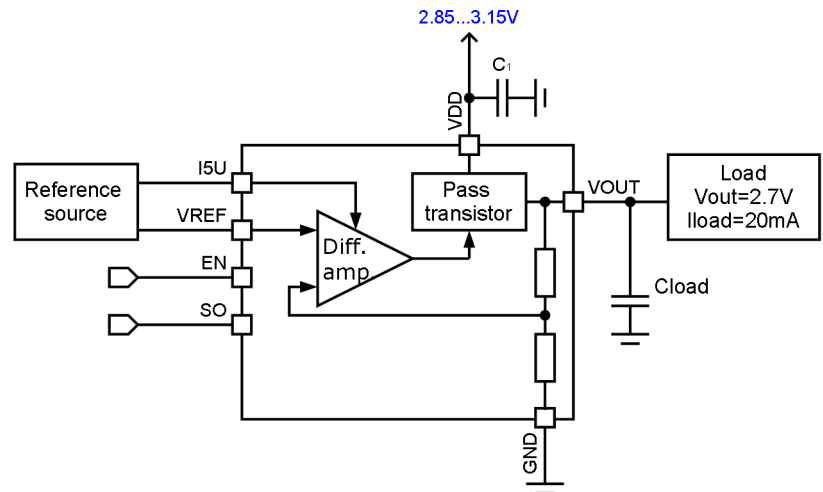
20mA LDO voltage regulator (output voltage 2.7V)
OVERVIEW

350AMS_LDOVR_01 the voltage regulator consists of differential amplifier which compares reference voltage 1.13V with voltage from feedback divider. It adjusts the impedance of the pass PMOS transistor for stabilization of output voltage at 2.7V.

IP technology: AMS035 BiCMOS 0.35 μm .

IP status: silicon proven.

Area: 0.018mm².


ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Conditions	Value			Units
			min	typ.	max	
Supply voltage	V_{DD}	-	2.85	3.0	3.15	V
Operating temperature range	T_j	-	-40	27	+85	$^{\circ}\text{C}$
Output voltage	V_{OUT}	-	-	2.7	-	V
Output voltage dependence of the input	D_{VOUT}	$I_{load} = 1\text{mA}$	-0.25	-	0.25	%
Reference voltage	V_{REF}	-	-	1.13	-	V
Input reference current	I_{I5U}	-	-	5	-	μA
Load capacitance	C_{load}	-	-	0.5	20	nF
Max load current	I_{load}	-	-	20	-	mA
Current consumption	I_{cc}	-	25	30	35	μA
Stand-by current	I_{SB}	-	-	<1	-	nA
Input logic-level low	V_{IL}	For digital signals	-0.2	-	0.2	V
Input logic-level high	V_{IH}		$0.9 \cdot V_{DD}$	-	V_{DD}	